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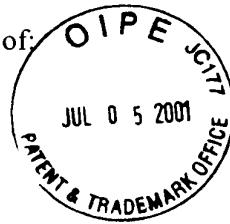
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of:

Shunpei YAMAZAKI

Serial No. 09/433,705

Filed: November 4, 1999

For: METHOD OF FABRICATING A
SEMICONDUCTOR DEVICE

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) Examiner: S. Loke

) Art Unit: 2811

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INFORMATION DISCLOSURE STATEMENT AND
NOTIFICATION OF RELATED APPLICATIONS

Commissioner for Patents
 Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the references listed on the Form PTO-1449 be made of record in the above-identified application.

The Examiner's attention is directed to the below listed related U.S. patent applications:

Serial No.	Filing Date
09/714,891	11/17/2000
09/619,732	07/19/2000
09/618,930	07/18/2000

The references listed on the enclosed Form PTO-1449 were previously cited in the aforementioned applications.

The Commissioner is hereby authorized to charge fees under 37 C.F.R. 1.16 and 1.17 (except the Issue Fee) which may be required now or hereafter, or credit any overpayment, to Deposit Account No. 19-2380.

Copies of the references are submitted herewith in accordance with 37 C.F.R. 1.98(a).

Respectfully submitted,


 Eric J. Robinson
 Registration No. 38,285

NIXON PEABODY LLP
 8180 Greensboro Drive, Suite 800
 McLean, Virginia 22102
 (703) 790-9110